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APPLICATION NO.	FILE	NG DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/013,983	12/10/2001		Nai-Shung Chang	JCLA7294	1948
7590 08/18/2004			EXAMINER		
J.C. Patents, Inc.				ELAMIN, ABDELMONIEM I	
Suite 250 4 Venture				ART UNIT	PAPER NUMBER
Irvine, CA 92	2618			2116	•
				DATE MAILED: 08/18/2004	1

Please find below and/or attached an Office communication concerning this application or proceeding.

			H(s)				
	Application	No. Applicant	(s)				
055	10/013,983	CHANG E	T AL.				
Office Action Summary	Examiner	Art Unit					
	A Elamin	2116					
The MAILING DATE of this commu Period for Reply	nication appears on the c	over sheet with the correspond	ence address				
A SHORTENED STATUTORY PERIOD THE MAILING DATE OF THIS COMMUI  - Extensions of time may be available under the provision after SIX (6) MONTHS from the mailing date of this con  - If the period for reply specified above is less than thirty If NO period for reply is specified above, the maximum  - Failure to reply within the set or extended period for rep Any reply received by the Office later than three months earned patent term adjustment. See 37 CFR 1.704(b).	NICATION.  ns of 37 CFR 1.136(a). In no event, nmunication. (30) days, a reply within the statutor statutory period will apply and will exalt will, by will, by statute, cause the applica	however, may a reply be timely filed  y minimum of thirty (30) days will be consid  xpire SIX (6) MONTHS from the mailing dat tion to become ABANDONED (35 U.S.C. §	te of this communication. § 133),				
Status							
1) Responsive to communication(s) fi	led on 10 December 200	<u>1</u> .					
2a)☐ This action is <b>FINAL</b> .	•						
3) Since this application is in conditio	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the prac	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) ⊠ Claim(s) <u>1-16</u> is/are pending in the 4a) Of the above claim(s) is/s 5) □ Claim(s) is/are allowed.  6) ⊠ Claim(s) <u>1-3,8,9 and 11-13</u> is/are of 7) ⊠ Claim(s) <u>4-7,10 and 14-16</u> is/are of 8) □ Claim(s) are subject to rest	are withdrawn from cons ejected. bjected to.						
Application Papers							
9) The specification is objected to by	the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any ob							
Replacement drawing sheet(s) including 11) The oath or declaration is objected	•						
Priority under 35 U.S.C. § 119	10 L) 110 L.M.						
	m for foreign priority undo	r 25 11 S C & 110(a) (d) or (f)					
12) Acknowledgment is made of a clair a) All b) Some * c) None of:  1. Certified copies of the priorit 2. Certified copies of the priorit 3. Copies of the certified copie application from the Internat * See the attached detailed Office act	y documents have been by documents have been s of the priority document ional Bureau (PCT Rule	received. received in Application No ts have been received in this N 17.2(a)).					
Attachment(s)		_					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date							
Notice of Draftsperson's Patent Drawing Review     Information Disclosure Statement(s) (PTO-1449     Paper No(s)/Mail Date	or PTO/SB/08) 5	Notice of Informal Patent Applica  Other:	ation (PTO-152)				

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#### **DETAILED ACTION**

#### Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-3, 8-9 and 11-13, are rejected under 35 U.S.C. 102(e) as being anticipated by Applicant's Admitted Prior Art "AAPA" (see the specification of the instant application pages 1-4).
- 3. Claims 1, 8 and 12, AAPA teaches a chipset supporting multiple CPU's, comprising:
- a first system logic circuit, applicable to the chipset and connected to a first type CPU [first system logic circuit 220 of Fig. 2, see the drawings of the instant application];
- a second system logic circuit, applicable to the chipset and connected to a second type CPU [second system logic circuit 230 of Fig. 2, see the drawings of the instant application];
- a double defined signal pin, used as a signal transmission pin [pin 241 of Fig. 2, see the drawings of the instant application];

an independent clock pin, coupled to the second system logic circuit as a clock signal pin, wherein the independent clock pin is isolated from other signal pins [pin 251 of Fig. 2, see the drawings of the instant application]; and

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a multiplex switch circuit [switch 240 of Fig. 2, see the drawings of the instant application], coupled to the first system logic circuit, the second system logic circuit and the double defined clock pin for establishing a first connection between the first system logic circuit and the double defined clock pin, and for establishing a second connection between the second system logic circuit and the double defined clock pin wherein the first type CPU may transfer signals with the first system logic circuit via the first connection and the second type CPU may transfer signals with the second system logic circuit via the second connection [Fig. 2, see the drawings of the instant application and the specification pages 1-4].

- 4. Claims 2, 9 and 13, AAPA teaches the first type CPU includes a Pentium series CPU and the second type CPU includes an AMD series CPU [see the specification page 2, lines 20-22].
- 5. Claim 3, AAPA teaches a frequency transferred by the independent clock pin is higher than a frequency transferred by the double defined signal pin [see the specification page 2, line 16 thru page 3, line 4].

#### Allowable Subject Matter

6. Claims 4-7, 10-11 and 14-16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A Elamin whose telephone number is (703)305-3804. The examiner can normally be reached on MON-FRI 9:30 AM - 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (703) 308-1159. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A Elamin Primary Examiner Art Unit 2116

August 14, 2004

A. ELAMIN PRIMARY EXAMINER